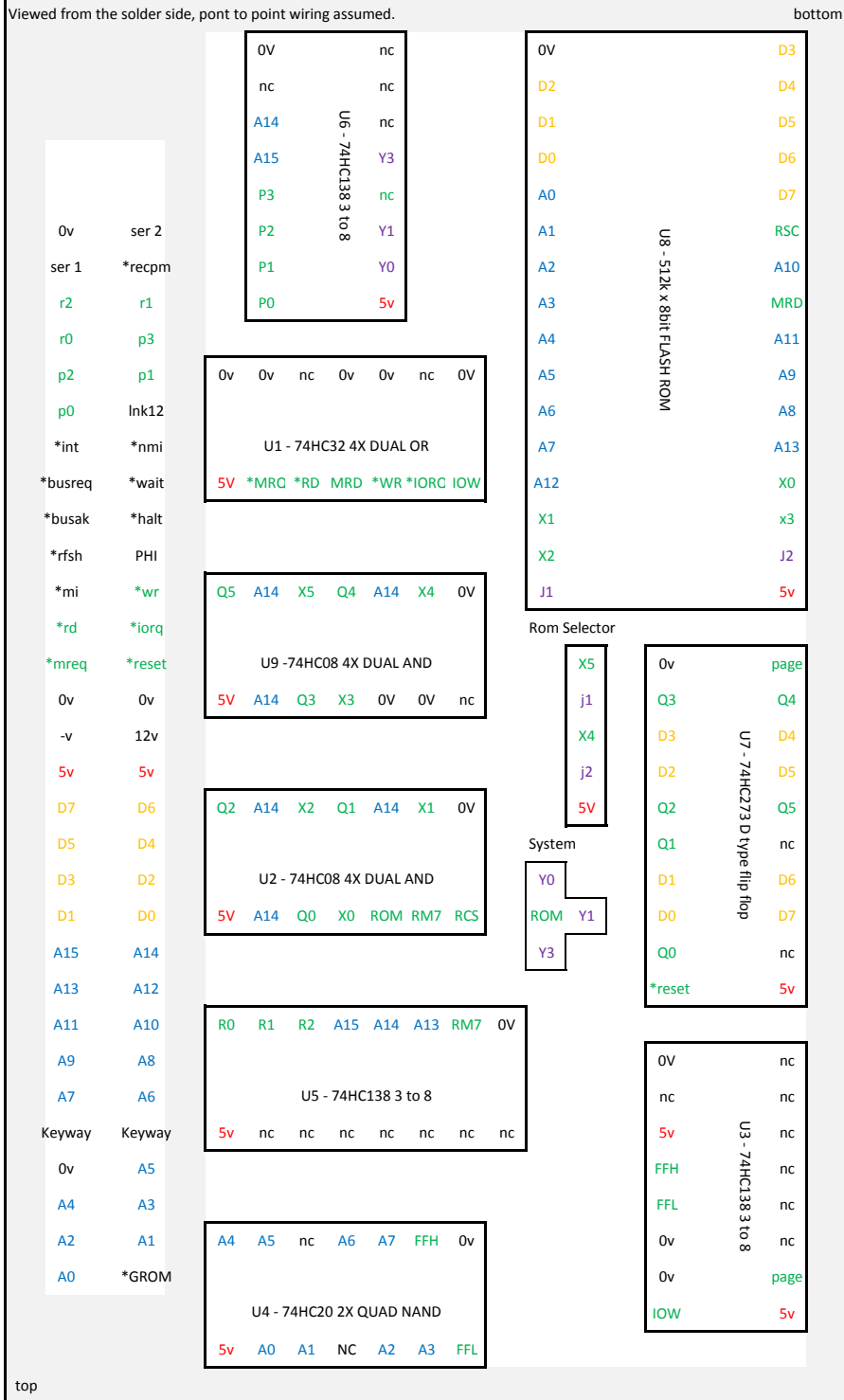


MTX Rom Board Version 3



Version 2 tested & working, version 3 adds the additional HC08 required for rom paging issue  
ROM does 2 jobs

Paged in as ROM 7 it's the controlling software  
Paged in bank 1 of the half ram page, it's the game data store

Paging for rom 7 - R15=0 R14=0 R13=1 R0=1 R1=1 R2=1, X0-X5 =0  
Paging for Bank1 R15=0 R14=1 P2=0 P3=0 P0/P1=0,1 or 3 for MTX500/MTX512/RS128  
Output port selected when IOWR low(via OR) and A0-A7 all high(via nand)

HC138 inputs 2 low one high CS, and 3 selectors  
for in situ write to flash, one of the spare gates in hc32 should be wired  
A and B inputs to \*WR and \*MREQ, Y output then replace 5v in the 5 pin jumper

J1 to Q4 J2 to 5v when fitting 128k or 512k flash

J1 to Q5, J2 to Q4 when fitting 1024k eeprom